# Introduction to Theoretical Computer Science Lecture 5: Starting on Computability

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# More Pigeonholes

Suppose a CFG has n non-terminals, and we have a parse tree of height k > n. What must have happened?

The same non-terminal  ${\it V}$  must have appeared as its own descendant in the tree.

### **Pumping for CFLs**

**Pumping down** Cut the tree at the higher occurrence of V and replace it with the subtree at the lower occurrence of V.

**Pumping up** Cut at the lower occurrence and replace it with a fresh copy of the higher occurrence.

# Pumping Lemma for CFLs

#### **Theorem**

If *L* is context-free then there exists a  $p \in \mathbb{N}$  (the pumping length) such that if  $w \in L$  with  $|w| \ge p$  then w may be split into **five** pieces w = uvxyz such that:

- **2** |vy| > 0 and
- $|vxy| \leq p$

It can be useful to think of it like a game:

- 1 You pick a language L
- Adversary picks a pumping length p
- **3** You pick a word  $w \in L$  with  $|w| \ge p$ .
- **4 Adversary** splits it into *uvxyz* s.t.  $|vxy| \le p$  and  $vy \ne \varepsilon$ .
- **§ You** win if you can find  $i \in \mathbb{N}$  such that  $uv^i xy^i z \notin L$ . Your prize is a proof of L not being context-free.

## Examples

## Example

Let  $L = \{a^i b^i c^i \mid i > 0\}$ . If L is a CFL it must have a pumping length p. Consider the word  $w = a^p b^p c^p$ . Then, we cannot avoid contradiction no matter how we split w = uvxyz:

If vxy is in  $a^*b^*$  then uxz (i.e.  $uv^0xy^0z$ ) is not in L because **condition 2** says vy contains at least one symbol. So uxz has fewer than p copies of a or b but still p copies of c. Similarly if vxy is in  $b^*c^*$ .

There are no other cases due to **condition 3**.

# Another example

Consider  $L = \{ww \mid w \in \{0, 1\}^*\}$ . If it is context free it must have a pumping length p > 0.

#### A rule of thumb

Pick a string w that allows as few cases for partitions of w = uvxyz as possible, to reduce the number of case distinctions.

Consider the word  $0^p 1^p 0^p 1^p$ . Let uvxyz = w such that  $|vxy| \le p$  and  $vy \ne \varepsilon$ . vxy can range over at most two of the four regions:

- If vxy is in a single one of the regions i.e.  $vxy \in 0^* \cup 1^*$  then pumping either way takes us out of L.
- Otherwise, if vxy spans some part of the first two or last two regions, i.e. a substring of 0<sup>p</sup>1<sup>p</sup>, pumping down will take us out of L.
- If vxy straddles the midpoint of w, pumping down will remove 1s from the first half but 0s from the second half, taking us out of L.

# **Chomsky Grammars**

CFGs are a special case of *Chomsky Grammars*. Chomsky Grammars are much like CFGs except that the left-hand side of a production may be any string that includes at least one non-terminal:

## Example

```
S \rightarrow abc \mid aAbc
Ab \rightarrow bA
Ac \rightarrow Bbcc
bB \rightarrow Bb
aB \rightarrow aaA \mid aa
```

This grammar is called **context-sensitive** 

# The Chomsky Hierarchy

#### Definition

A grammar  $G = (N, \Sigma, P, S)$  is of *type*:

- (or *computably enumerable*) in the general case.
- **1** (or *context-sensitive*) if  $|\alpha| \le |\beta|$  for all productions  $\alpha \to \beta$ , except we also allow  $S \to \varepsilon$  if S does not occur on the RHS of any rule.
- **2** (or *context-free*) if all productions are of the form  $A \rightarrow \alpha$  (i.e. a CFG).
- **3** (or *right-linear*) if all productions are of the form  $A \to w$  or  $A \to wB$  where  $w \in \Sigma$  and  $B \in N$ .
- Recursively enumerable is also called *Turing-recognisable*.
- Right-linear is also called...regular!

# **Emptiness**

Can we write a computer program to determine if a given regular language is empty?

## Emptiness for regular languages

Given a **finite automaton**, this is an instance of *graph reachability* — can we reach a final state? Can be done via depth-first search. Given a **regular expression**, we can work **inductively** (see board).

# **Emptiness Continued**

Can we write a computer program to determine if a given context-free language is empty?

## **Emptiness of CFLs**

Given a CFG for our language:

- **1** Mark the terminals and  $\varepsilon$  as generating.
- 2 Mark as generating all non-terminals which have a production with only generating symbols in their RHS.
- 3 Repeat until nothing new is marked generating.
- Check whether S is marked as generating.

# Equivalence

Can we write a computer program to determine if two given DFAs are equivalent?

## Equivalence of Regular Languages

Given two DFAs for  $L_1$  and  $L_2$  we can use our standard constructions to produce a DFA of the symmetric set difference:

$$(L_1 \cap \overline{L_2}) \cup (L_2 \cap \overline{L_1})$$

(Constructions for complement and intersection are in coursework 1, not lectures.) If this DFA is empty, then the two languages are equal.

## **Equivalence Continued**

Later we'll develop a theory that allows us to prove rigorously that there are problems that cannot be solved by any algorithm that can be implemented as a conventional computer program.

Such problems are called undecidable.

Many undecidable problems exist for CFLs:

- Are two CFGs equivalent?
- Is a given CFG ambiguous?
- Is there a way to make a CFG unambiguous?
- Is the intersection of two CFLs empty?
- Does a CFG generate all strings  $\Sigma^*$  (also called *universality*)

# Register Machines

## Key Insight

#### There is a general model of computation

You may have heard of the *Turing Machine*, but we will first focus on something closer to our understanding of programs.

#### **Definition**

A register machine, or RM, consists of:

- A **fixed** number m of *registers*  $R_0 \dots R_{m-1}$ , which each hold a natural number.
- A **fixed** program P which is a sequence of n instructions  $I_0 \ldots I_{n-1}$  Each instruction is either: INC(i), which increments register  $R_i$ , or

Each instruction is either: INC(i), which increments register  $R_i$ , or DECJZ(i,j) which decrements  $R_i$  unless  $R_i = 0$  in which case it jumps to  $I_j$ .

# Questions of RMs

What can we compute with RMs? What is unrealistic about them?

#### Claim

RMs can compute anything any other computer can.

#### **RM ASM**

#### **Problem**

Programming in RMs directly is very tedious and programs can be overlong.

We will use some simple notation similar to assembly language to simplify it.

#### Macros

- We'll write them in English, e.g. "add  $R_i$  to  $R_j$  clearing  $R_i$ ".
- When defining a macro, we'll number instructions from zero, but the instructions are renumbered when macros are expanded. We also use symbolic labels for jumps.
- Macros can use special, negative-indexed registers, guaranteed not to be used by normal programs.

```
Goto I_i using R_{-1} as temp
0 DECJZ (-1,j)
Clear R:
0 DECJZ (i, 2)
1 GOTO 0 (using macro above)
Copy R_i to R_i using R_{-2} as temp
        0 CLEAR Ri
loop_1: 2 DECJZ (i, loop_2)
        3 INC (j)
        4 INC (-2)
        5 GOTO loop<sub>1</sub>
        6 DECJZ
                  (-2, end)
 loop2:
          INC
               (i)
          GOTO
                  loop2
   end
```

# **RM Programming Exercises**

- Addition and subtraction of registers
- Comparison of registers
- Multiplication of registers
- Division/Remainder of registers

# How many registers?

So far, we've just assumed we had as many registers as we needed. But how many do we actually need?

## Pairing functions

A *pairing function* is an injective function  $\mathbb{N} \times \mathbb{N} \to \mathbb{N}$ .

An example is  $f(x, y) = 2^x 3^y$ .

We write  $\langle x, y \rangle_2$  for f(x, y). If  $z = \langle x, y \rangle_2$ , let  $z_0 = x$  and  $z_1 = y$ .

**Exercise**: Program a pairing function and unpairing functions on a RM.

**Exercise**: Design (or look up) a surjective pairing function.

## Generalising

Just a 2-tuple pairing function is enough to cram an arbitrary sequence of natural numbers into one  $\mathbb{N}^* \to \mathbb{N}$ .

## Conclusion

With pairing functions, we can simulate any number of registers using just the registers we need to compute the pairing and unpairing functions, and one user register.

## Question

So, how many registers do we actually need?